#### TOSHIBA Transistor Silicon NPN Epitaxial Type

# 2SC5714

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

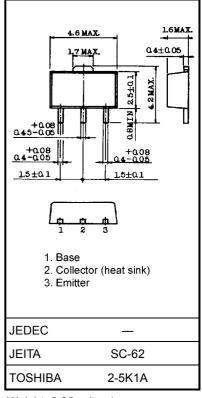
- High DC current gain:  $h_{FE} = 400$  to 1000 (I<sub>C</sub> = 0.5 A)
- Low collector-emitter saturation voltage:  $V_{CE}$  (sat) = 0.15 V (max)
- High-speed switching:  $t_f = 90 \text{ ns}$  (typ.)

#### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	40	V	
Collector-emitter voltage		V <sub>CEX</sub>	30	V	
Collector-emitter voltage		V <sub>CEO</sub>	20	V	
Emitter-base voltage		V <sub>EBO</sub>	7	V	
Collector current	DC	Ι <sub>C</sub>	4	A	
	Pulse	I <sub>CP</sub>	7		
Base current		Ι <sub>Β</sub>	400	mA	
Collector power dissipation	DC	P <sub>C</sub>	1.0	W	
	t = 10 s	(Note)	2.5		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Industrial Applications

Unit: mm



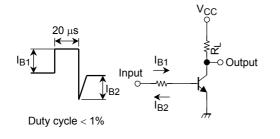
Weight: 0.05 g (typ.)

## **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = 40 \text{ V}, I_E = 0$	_		100	nA	
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB}=7~V,~I_C=0$	_	_	100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	20	_	_	V	
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = 2 V, I_C = 0.5 A$	400	_	1000		
		h <sub>FE</sub> (2)	$V_{CE} = 2 V, I_C = 1.6 A$	200	_	_		
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_{C} = 1.6 \text{ A}, I_{B} = 32 \text{ mA}$	_		0.15	V	
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	$I_{C} = 1.6 \text{ A}, I_{B} = 32 \text{ mA}$	_	_	1.10	V	
Collector output capacitance		C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	18	_	pF	
Switching time	Rise time	tr	See Figure 1 circuit diagram.	_	100	_		
	Storage time	t <sub>stg</sub>	$V_{CC} \simeq 12$ V, $R_L = 7.5$ $\Omega$		350		ns	
	Fall time	t <sub>f</sub>	$I_{B1} = -I_{B2} = 53.3 \text{ mA}$		90			

Note: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)

Marking



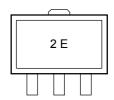
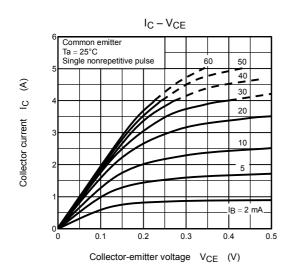
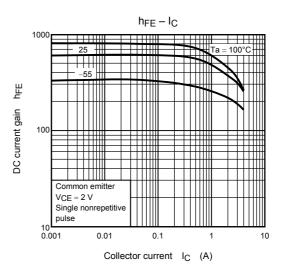
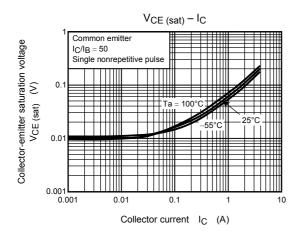


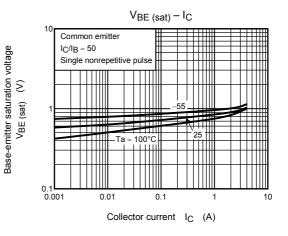
Figure 1 Switching Time Test Circuit & Timing Chart

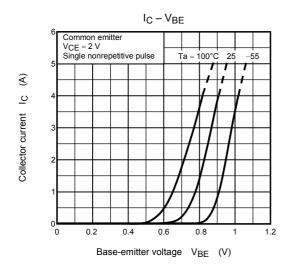
# TOSHIBA

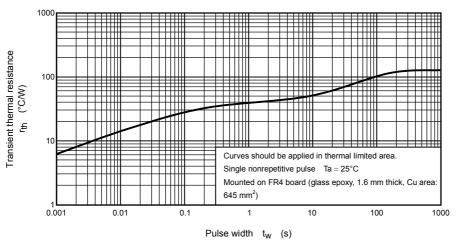












Transient Thermal Resistance rth - tw

Safe Operating Area <sup>10</sup> FIC max (pulsed) ♦ 10 μs♦ -IC max (continuous) \_100 μs♦ \_1 ms • \_10 ms♦ € =DC operation \* -100 ms♦ -10 s♦\* <u>ں</u> | | | | Collector current ◆: Single nonrepetitive pulse Ta = 25°C Note that the curves for 100 ms\*, 10 s\* and DC operation\* will be different when the devices aren't mounted on an FR4 board (glass epoxy, 1,6 mm thick, Cu area: 645 mm<sup>2</sup>). These characteristic curves must be derated linearly with increase in temperature. 0.1 +++++ /CEO max increase in temperature 0.01 10 100 Collector-emitter voltage  $V_{CE}$  (V)

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